



Curriculum vitae Europass



Personal information

Name / First name **TANASE Cristian Andy**
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 Nationality(s) Romanian
 Date of birth 04-02-1974
 Sex M

Occupational field

**Associate Professor– Faculty of Electrical Engineering and Computer Science,
 Ștefan cel Mare University of Suceava**

Professional experience

Period	10.1998 - 10.2004	10.2004- 03.2021	03.2021-present	01.1999- 10.2001	11.2001- 12.2002	12.2011- 09.2014
Position or position occupied	Assistant	Lecturer	Associate Professor	Hardware Engineer	Software Engineer	Software Engineer
Main activities and responsibilities	VLSI Design, Digital Signal Processing, Parallel Calculation Architectures	VLSI Design, Digital Signal Processing, Parallel Calculation Architectures	VLSI Design, Digital Signal Processing, Parallel Calculation Architectures, Assembly Languages	Development of Electronic Systems	VLSI Design	SoC Design and Verification (Systems on Chip)
Name and address of employer	Ștefan cel Mare University of Suceava, Faculty of Electrical Engineering and Computer Science	Ștefan cel Mare University of Suceava, Faculty of Electrical Engineering and Computer Science	Ștefan cel Mare University of Suceava, Faculty of Electrical Engineering and Computer Science	ADVANCED CONTRACT ELECTRONI CS.R.L. (Fontane ACM Group Germany)	ISRATECH S.R.L Suceava, Romania	SILICON SERVICE, Iasi, Romania (AMD Inc. USA Contractor)
Type of activity or sector of activity	Teaching	Teaching	Teaching	Researchers	Researchers	Researchers

Education and training

Period	10.1992- 06.1998	10.1999-06-2000	10.2001-09.2008	10.2004-02.2009	10.2010-02.2013

Qualification / diploma obtained	Bachelor's degree in Computer Science	Degree of In-Depth Studies – Intelligent Systems for Process Control	PhD in Electronic Engineering and Telecommunications "Contributions to the development of assisted design techniques of integrated structures"	Bachelor's degree in Accounting and Management Informatics	Post-doctoral research "Implementation of parallel algorithms in solving the problem of searching for a mobile target in a dynamic environment"
Name and type of educational establishment/training provider	Ștefan cel Mare University of Suceava, Faculty of Electrical Engineering and Computer Science	Ștefan cel Mare University of Suceava, Faculty of Electrical Engineering and Computer Science	Ștefan cel Mare University of Suceava, Faculty of Electrical Engineering and Computer Science	Ștefan cel Mare" University of Suceava, Faculty of Economic Sciences and Public Administration	Ștefan cel Mare University of Suceava, Faculty of Electrical Engineering and Computer Science

Personal skills and competences

Native language Romanian

Known foreign language(s)

Self-assessment
European level (*)

English

Italian

French

Understanding		Speech		Writing
Listening	Read	Join the conversation	Oral speech	Written expression
B2	B2	B2	B2	B2
A2	A2	A2	A2	A2
A1	A1	A1	A1	A1

(*) Level of the Common European Framework of Reference for Foreign Languages

Social skills and competences

Good communication skills acquired through his own experience as a teacher at the "Ștefan cel Mare" University in Suceava

Organisational skills and competences

Member of the implementation team of the following projects:

Project Director:

- CONTRIBUTII LA DEZVOLTAREA TEHNICILOR DE PROIECTARE ASISTATA A STRUCTURILOR INTEGRATE, Contract de cercetare din Planul National de Cercetare, Dezvoltare și Inovare - PN II, Program: Resurse Umane, Proiecte de cercetare pentru tineri doctoranzi - tip TD, Septembrie 2007, Contract Nr. 161/01-10-2007 - **director proiect Cristian Andy TĂNASE, USV**

Member of the research team:

- PNCDI III, Modul CERN-RO, Program 5, Subprogram 5.2; LHCb: contract nr. 7/16.03.2016, "LHCb - Studiul producției de hadroni, fizica "aromelor" în particule elementare grele și programul de upgrade (al detectorului)", director de proiect: Dr. Florin MACIUC, IFIN-HH, 2016 - 2018
- PN III: Programul 2 - Creșterea competitivității economiei românești prin cercetare, dezvoltare și inovare - "Demonstrator experimental de laborator bazat pe nHSE - sistem de operare de timp real integrat în hardware - implementat pe o arhitectură ZScale - RISC V"; NR. 219PED/2017, director de proiect Dr. Vasile Gheorghită GĂITAN, USV
- PN III: Programul 2 - Creșterea competitivității economiei românești prin cercetare, dezvoltare și inovare - "Dispozitiv experimental demonstrativ pentru validarea și testarea microcontrolerului nMPRA de timp real utilizând arhitectura MIPS32"; NR. 220PED / 2017, director de proiect Dr. Cristina Nicoleta GĂITAN, USV
- Proiect POSDRU/89/1.5/S/57083, "Progres și dezvoltare prin cercetare și inovare post – doctorală în inginerie și științe aplicate (POST DOC — PRiDE)", director de proiect: Valentin Popa, USV

Relevant publications:

ISI:

- 1) Detecting and Tracking Multiple Users in the Proximity of Interactive Tabletops - *Tanase, Cristian Andy; Vatavu, Radu-Daniel; Pentiu, Stefan-Gheorghe; Graur, A* - ADVANCES IN ELECTRICAL AND COMPUTER ENGINEERING, Volume: 8, Issue: 2, Pages: 61-64, Published: 2008, IF2019 – **1.102**
- 2) Autonomous Mobile Device Controlled by On-chip Network of Intelligent Sensors for Indoor Environment Navigation - *Tanase, Cr. A.; Graur, A.; Gaitan, V. G.; Popa, V* - ELEKTRONIKA IR ELEKTROTEHNIKA, Issue: 5, Pages: 77-82, Published: 2009, IF2019 – **0.707**
- 3) Dynamic, unbalanced distribution of tasks on a PS3 cluster system for double precision calculation - *Tanase, Cristian Andy; Gaitan, Vasile Gheorghita* - JOURNAL OF SUPERCOMPUTING, Volume: 62, Issue: 3, Pages: 1502-1518, Published: DEC 2012, IF2019 – **2.469, Q2**
- 4) Threads Pipelining on the CellBE Systems - *Tanase, Cristian Andy; Gaitan, Vasile Gheorghita* - ADVANCES IN ELECTRICAL AND COMPUTER ENGINEERING, Volume: 13, Issue: 3, Pages: 121-126, Published: 2013, IF2019 – **1.102**
- 5) An approach of MPRA technique over ARM cache architecture - *Tanase, Cristian Andy* - 13th International Conference on Development and Application Systems (DAS), Suceava, ROMANIA, Date: MAY 19-21, 2016, Sponsor(s): Stefan cel Mare Univ Suceava, Fac Elect Engn & Comp Sci Romania; IEEE Ind Applicat Soc, Romania Sect 2016 13TH INTERNATIONAL CONFERENCE ON DEVELOPMENT AND APPLICATION SYSTEMS (DAS 2016), Pages: 86-90, Published: 2016
- 6) Hardware Real-time Event Management with Support of RISC-V Architecture for FPGA-Based Reconfigurable Embedded Systems - *Zagan, Ionel; Tanase, Cristian Andy; Gaitan, Vasile Gheorghita* - ADVANCES IN ELECTRICAL AND COMPUTER ENGINEERING, Volume: 20, Issue: 1, Pages: 63-70, Published: FEB 2020, IF2019 – **1.102**
- 7) Dynamic scheduler implementation used for load distribution between hardware accelerators (RTL) and software tasks (CPU) in heterogeneous systems - *Tanase, Cristian Andy* - JOURNAL OF SUPERCOMPUTING, Volume: 76, Issue: 12, Pages: 10122-10139, Published: DEC 2020, IF2019 – **2.469, Q2**
- 8) Reducing Energy Consumption in Microcontroller-based Systems with Multipipeline Architecture - *Tanase, Cristian Andy* - INTERNATIONAL JOURNAL OF ADVANCED COMPUTER SCIENCE AND APPLICATIONS, Volume: 11, Issue: 12, Pages: 16, Published: DEC 2020

Books:

- 1) Andy Tănase, Vasile Găitan - Familia de Procesoare pentru Prelucrarea Numerică a Semnalelor ADSP-21xx. Editura Matrix, București 2002. ISBN 973-685-356-X
- 2) Vasile Gheorghită Găitan, Valentin Popa, Andy Cristian Tănase – Arhitectura Rețelelor Industriale Locale. Editura Matrix, București 2004. ISBN 973-685-849-9
- 3) Andy Cristian Tănase – Programarea Sistemelor de Prelucrare Numerică a Semnalelor, Editura Universității Ștefan cel Mare Suceava 2010. ISBN 978-973-666-331-4
- 4) Andy Cristian Tănase, Cătălin Marian Sîicu – Prelucrarea Numerică a Semnalelor, Îndrumar de laborator, Editura Universității Suceava, 2004

Technical skills and competences	<ul style="list-style-type: none"> ▪ Dedicated Digital Signal Processing Systems (Software Development Tools and Hardware Analog Devices – ADSP2181, ADSP BF535); ▪ SoC Design and Verification (Systems on Chip): <ol style="list-style-type: none"> 1) Xilinx FPGA family - Virtex and ZYNQ UltraScale+; 2) Vivado Development Tools, Vitis SDK, Vitis HLS, Model Composer MATLAB/Simulink; 3) HDL Languages: Verilog, VHDL, SystemC/C++ (including siftable versions). ▪ Programming and Design of Embedded Systems ▪ High-level programming (C, C++, Visual C, Python) ▪ Assembly level programming (x86, ARM, MIPS) ▪ PCB Design (Orcad, Protel-Altium Designer) ▪ Parallel and Real Time Programming (Cell BE, ZYNQ UltraScale+)
Computer skills and competencies	Experienced user
Other skills and competences	
Driving license(s)	B